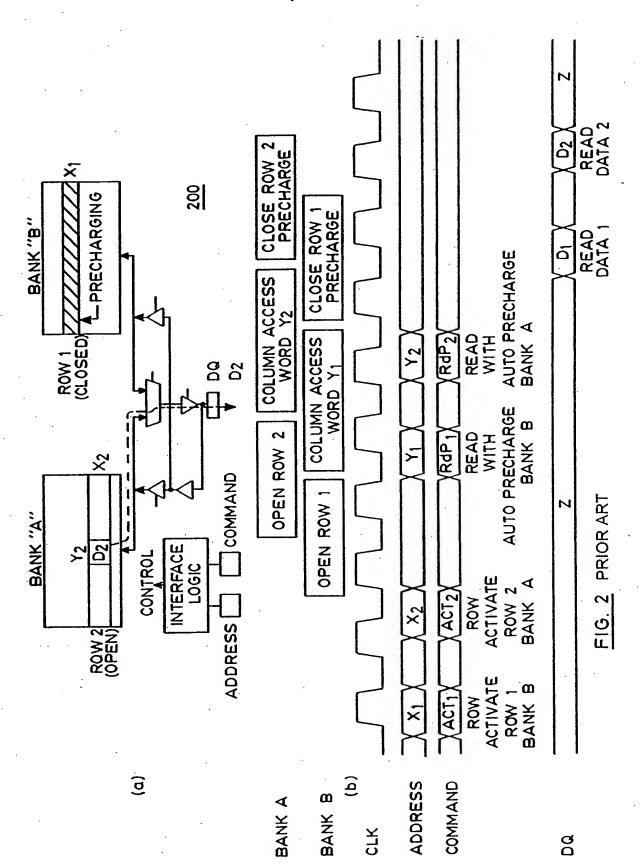
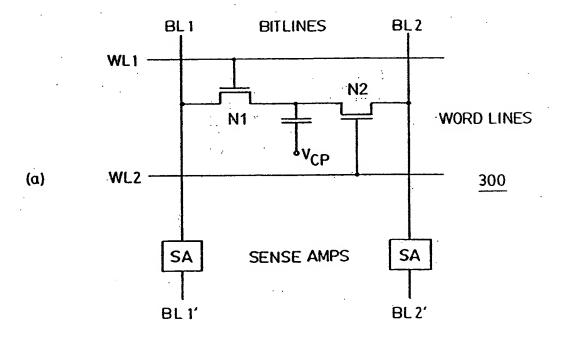


FIG. 1 PRIOR ART





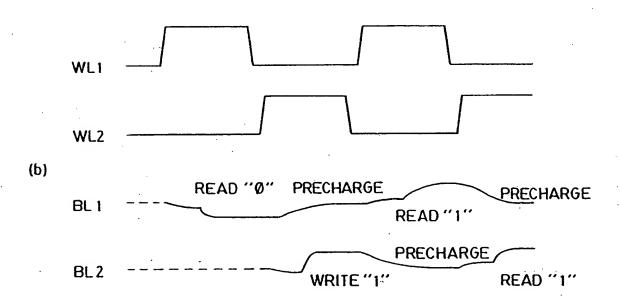


FIG. 3 PRIOR ART



007

CELL CHARGE ---+%56

+ %09

9

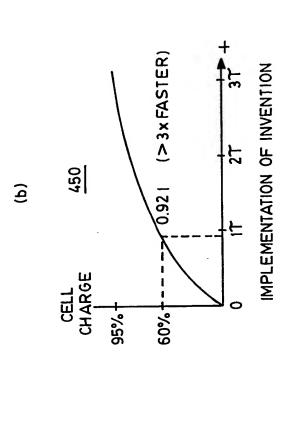
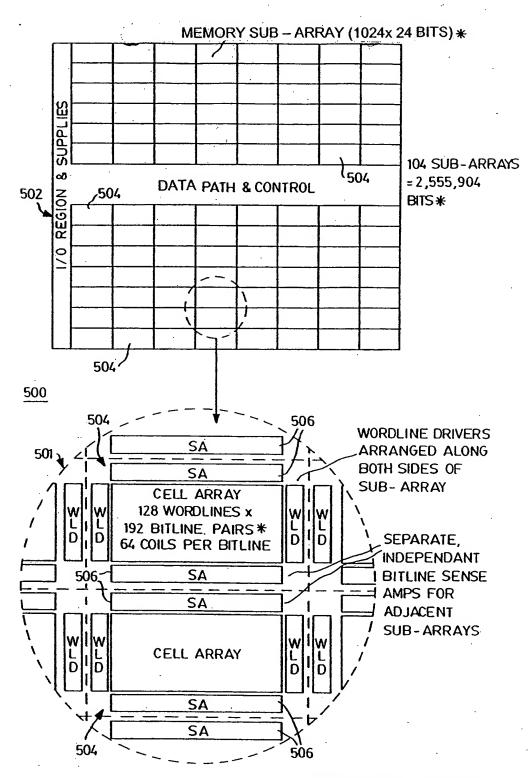


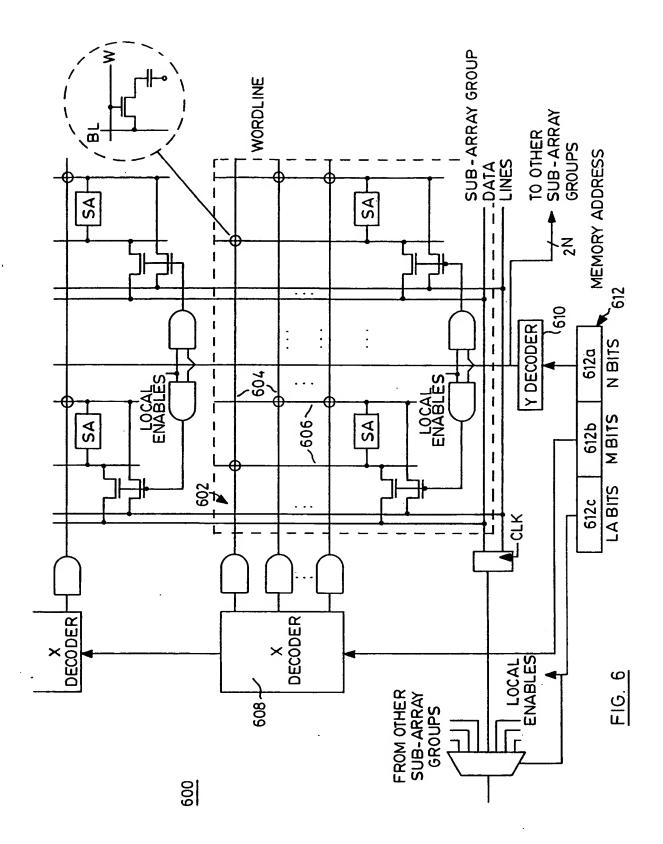
FIG. 4

CONVENTIONAL DRAM

2 T

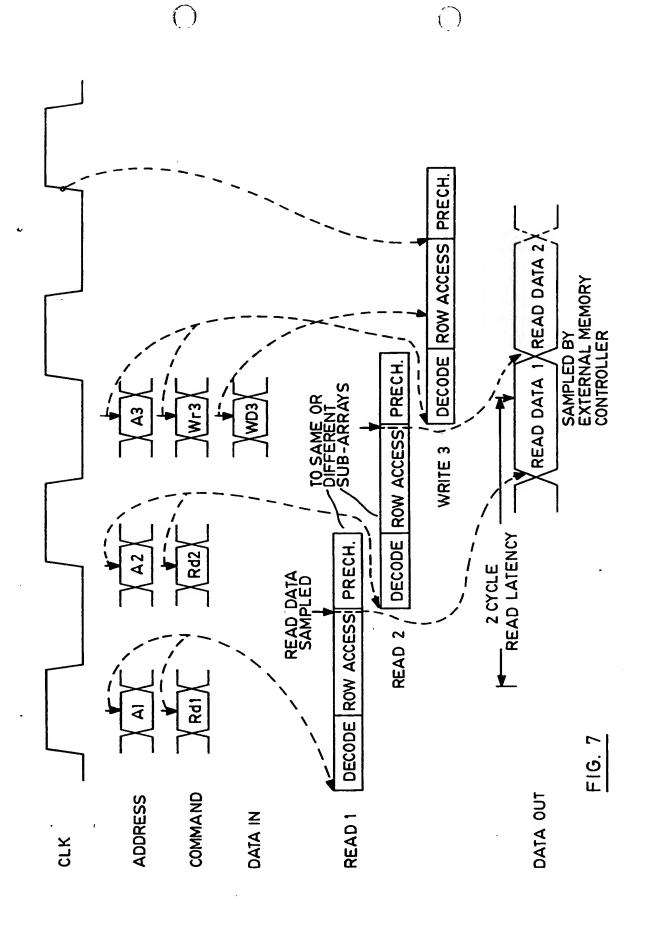


*REDUNDANT ELEMENTS NOT INCLUDED



 \bigcirc

 \bigcirc



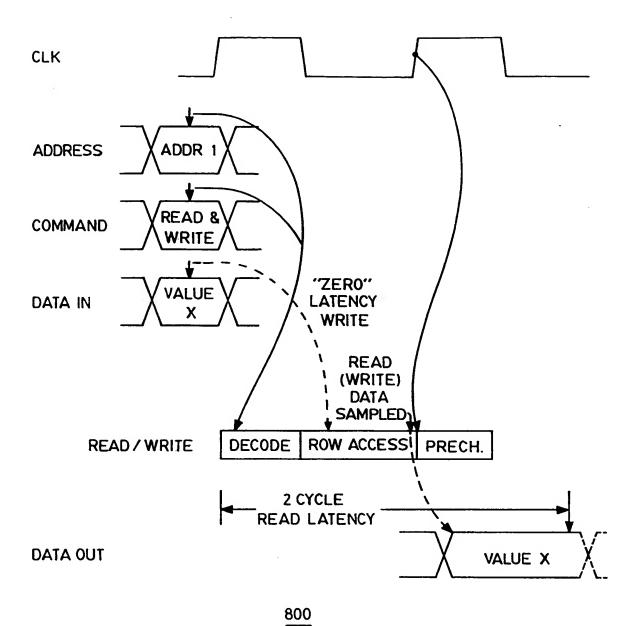
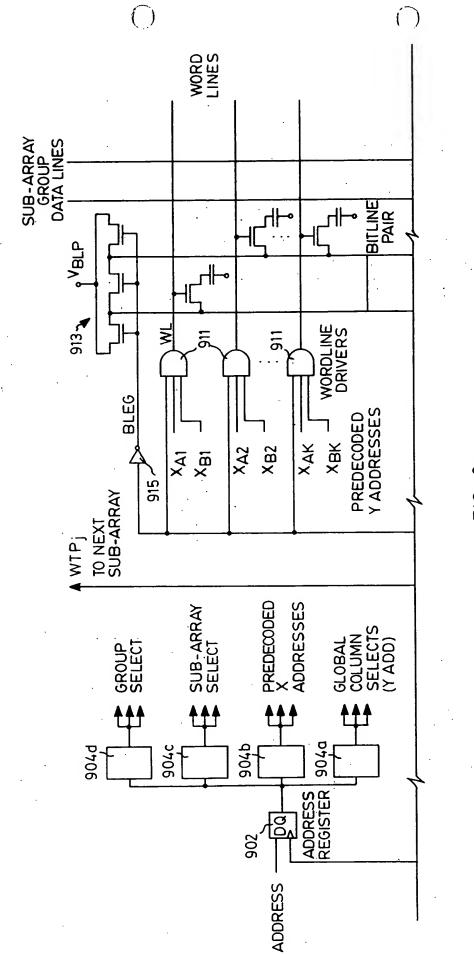
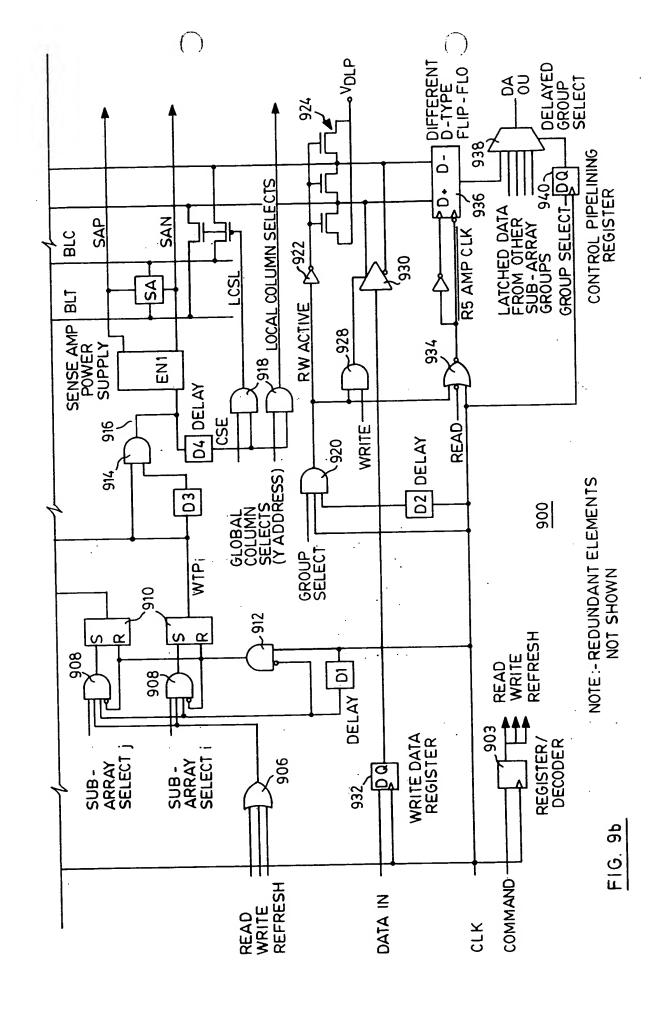


FIG. 8



F1G. 9a



. چ

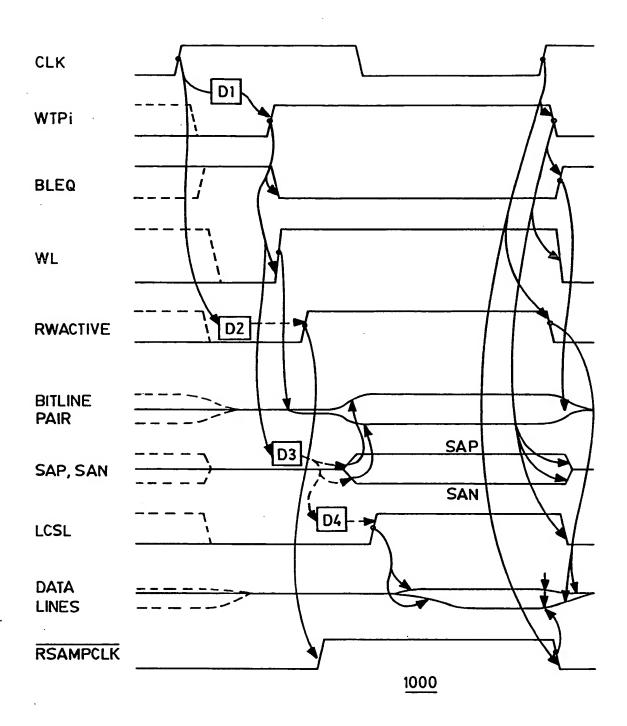


FIG. 10a

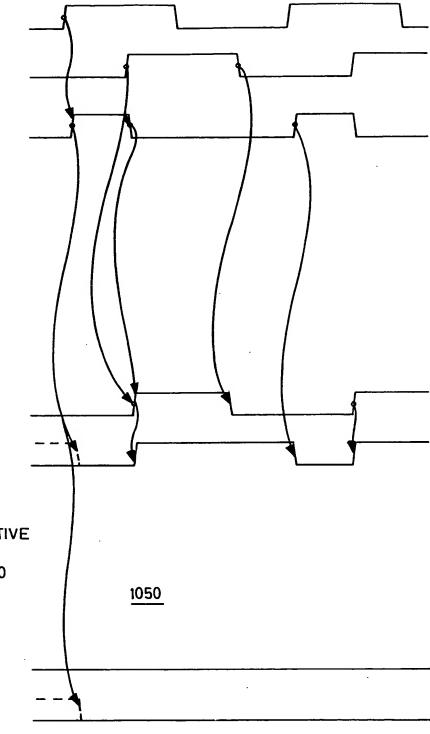


FIG. 10b

CLK

RACCLK (DELAYED CLK)

R

CASE 1

SELECTED, ACTIVE SUB-ARRAY i (ARRAY SELECT i = 1 AND (READ = 1 OR WRITE = 1 OR REFRESH = 1))

S

WTPi

CASE 2

UNSELECTED, INACTIVE
SUB-ARRAY i
(ARRAY SELECT i = 0
OR
(READ = 0 AND
WRITE = 0 AND
REFRESH = 0))
S

 WTP_i

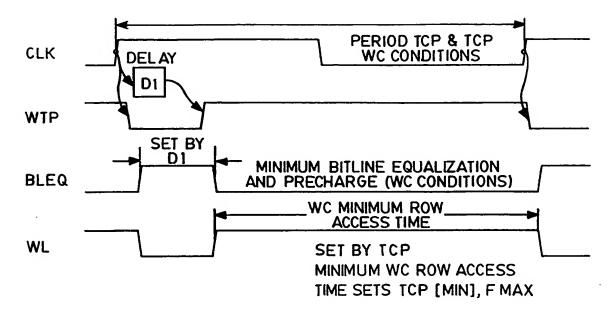


FIG. 11a

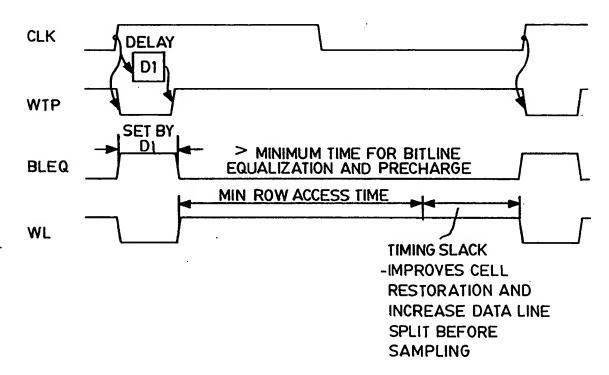
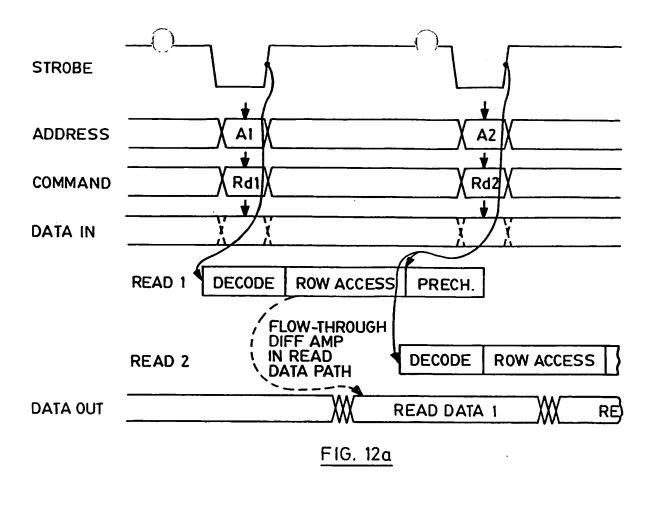


FIG. 11b



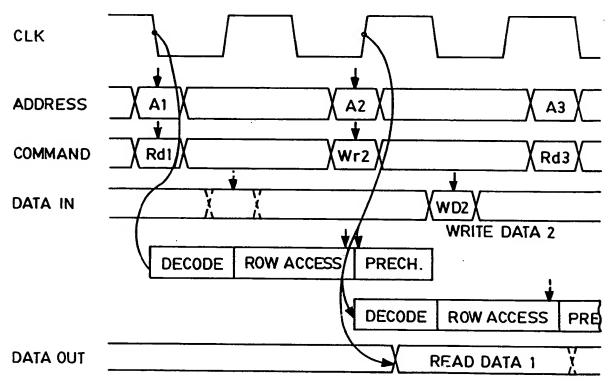


FIG. 12b

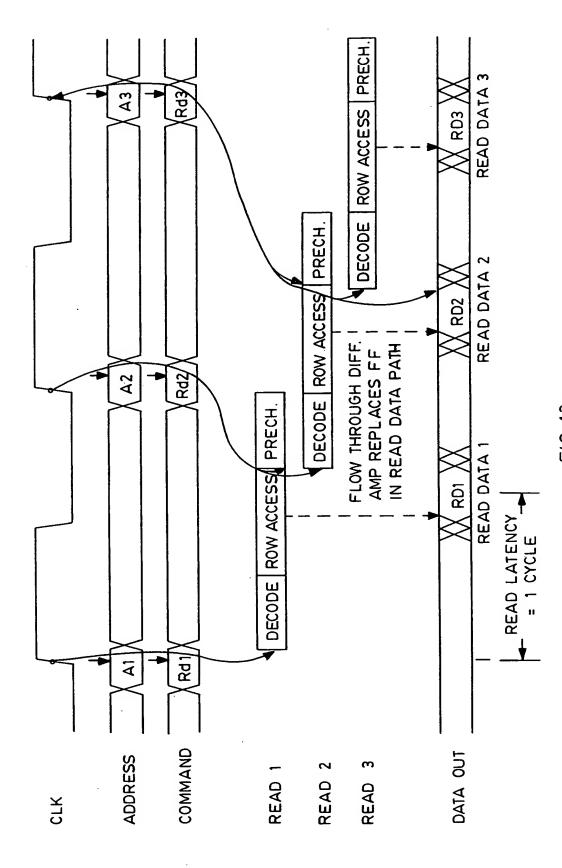


FIG. 13a

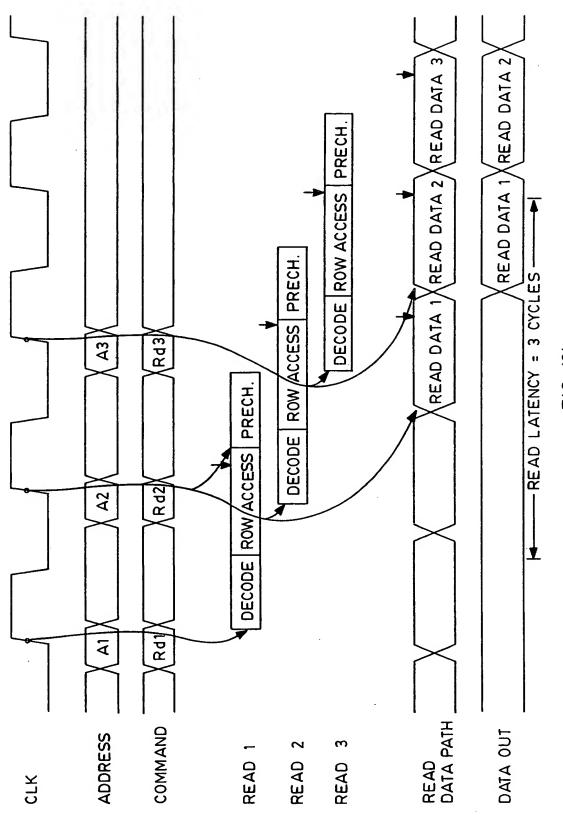


FIG. 13b